

Original document

INTELLIGENT NETWORK INTERFACE DEVICE AND SYSTEM FOR ACCELERATING COMMUNICATION

Patent number: DE116118T
Publication date: 2003-08-14
Inventor: BOUCHER B [US]; BLIGHTMAN E [US]; CRAFT K [US]; HIGGEN A [US]; PHILBRICK M [US]; STARR DARYL [US]
Applicant: ALACRITECH INC [US]; BOUCHER LAURENCE B [US]; BLIGHTMAN STEPHEN E J [US]; CRAFT PETER K [US]; HIGGEN DAVID A [US]; PHILBRICK CLIVE M [US]; STARR DARYL [US]
Classification:
- international: G06F13/00
- european:
Application number: DE19980959558T 19981120
Priority number(s): US19980141713 19980828; WO1998US24943 19981120

Also published as:

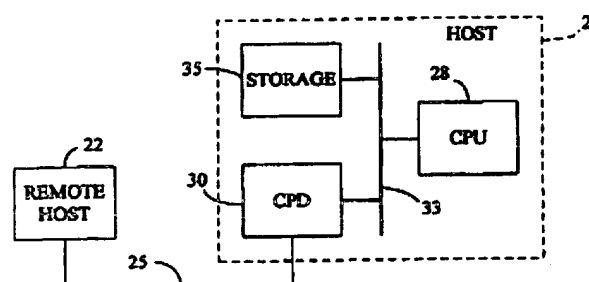
WO0013091 (A)
EP116118 (A1)
CA2341211 (A1)

View INPADOC patent family

Abstract not available for DE116118T

Abstract of corresponding document: **WO0013091**

An intelligent network interface card or communication processing device (30) works with a host computer (20) for data communication. The device provides a fast-path (159) that avoids protocol processing for most messages, greatly accelerating data transfer and offloading time-intensive processing tasks from the host CPU (28). The host retains a fallback processing capability for messages that do not fit fast-path criteria, with the device providing assistance such as validation even for slow-path messages, and messages being selected for either fast-path or slow-path (158) processing. A context (50) for a connection is defined that allows the device to move data, free of headers, directly to or from a destination or source in the host. The context can be passed back to the host for message processing by the host. The device contains specialized hardware circuits that are much faster at their specific tasks than a general purpose CPU. A preferred embodiment includes a trio of pipelined processors (482, 484, 486) devoted to



receive, transmit and utility processing, providing full duplex communication for four Fast Ethernet nodes.

Data supplied from the *esp@cenet* database - Worldwide